



## PATENT APPLICATION

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Yoshitaka SASAGO et al.

Appln. No.: 10/701,497 Group Art Unit: 2822

Filed: November 6, 2003

Examiner: T. M. THOMAS

For: NONVOLATILE SEMICONDUCTOR MEMORY DEVICE AND

MANUFACTURING METHOD THEREOF

**AMENDMENT** 

Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450

Sir:

In response to the Office Action dated December 19, 2005, please amend this application as indicated below.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 13 of this paper.